



## COURSE OUTLINE

### ELEC 569A – Selected Topics in Computer Engineering: Low Power Design Summer 2014

**Instructor:**

Dr. Amirali Baniasadi

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**Office Hours:**

Days: by appointment via 721-8613, or email

Time:

Location:

**Lectures:**

A-Section(s): A01 / CRN 30331

Days: Mondays

Time: 15:00 – 17:50

Location: ECS 108

**Labs:**

B-Section(s)

**Location: ELW**

Days

Time

**Required Text:**

Title: Power Aware Design Methodologies

Author: M. Pedram and J. M. Rabaey

Publisher: Springer

**References:****Assessment:**

Project or Presentations

Final

50% Date: Will be announced in advance

50%

**Due dates for assignments:**

The final grade obtained from the above marking scheme will be based on the following percentage-to-grade point conversion:

<b>Passing Grades</b>	<b>Grade Point Value</b>	<b>Percentage For Instructor Use Only</b>	
A+	9	90 - 100	
A	8	85 - 89	
A-	7	80 - 84	
B+	6	77 - 79	
B	5	73 - 76	
B-	4	70 - 72	
C+	3	65 - 69	
C	2	60 - 64	
D	1	50 - 59	
<b>Failing Grades</b>	<b>Grade Point Value</b>	<b>Percentage For Instructor Use Only</b>	<b>Notes</b>
E	0	35 - 49	Fail, conditional supplemental exam.
F	0	0 - 34	Fail, no supplemental exam.
N	0	0 - 49	Did not write examination, Lab or otherwise complete course requirements by the end of the term or session; no supplemental exam.

The rules for supplemental examinations are found on page 80 of the current 2013/14 Undergraduate Calendar.

<b>Term in which E Grade was obtained:</b>	<b>Application Deadline for Supplemental Exam</b>	<b>Supplemental Exam Date</b>
First term of Winter Session (Sept - Dec)	Following February 28	First week of following May
Second term of Winter Session (Jan - Apr)	Following June 30	First week of following September
Summer Session (May - Aug)	Following October 31	First week of following January

Deferred exams will normally be written at the start of the student's next academic term; i.e., approximately 4 months following the deferral of the exam.

1. **Course Objectives:** Understanding and analyzing low power system optimizations.
2. **Learning Outcomes:** Learning to Analyze and Design low-power circuits and microarchitectures.
3. **Syllabus:** Low-Power Branch Predictors, Low-Power Execution Units, Low-Power Memory Systems, Low-Power Register Files

### **Guidelines on Religious Observances**

See <http://web.uvic.ca/calendar2014/GI/GUPo.html>

### **Commitment to Inclusivity and Diversity**

The University of Victoria is committed to promoting, providing and protecting a positive, supportive and safe learning and working environment for all its members.

## **Standards of Professional Behaviour**

You are advised to read the Faculty of Engineering document Standards for Professional Behaviour at <http://www.engr.uvic.ca/policy/professional-behaviour.php> which contains important information regarding conduct in courses, labs, and in the general use of facilities.

Cheating, plagiarism and other forms of academic fraud are taken very seriously by both the University and the Department. You should consult

<http://web.uvic.ca/calendar2014/FACS/UnIn/UARe/PoAcI.html> for the UVic policy on academic integrity.

**Plagiarism detection software may be used to aid the instructor and/or TA's in the review and grading of some or all of the work you submit.**